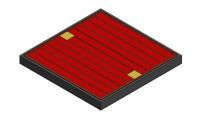


This die specification contains the basic features of the 6th generation high current thinfilm dice from OSRAM Opto Semiconductors. Remarkable light extraction is reached by a particular top emitting design with vertical chip structure. Furthermore the LED die shows excellent reliability performance.



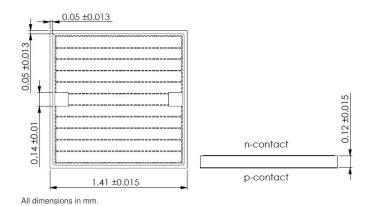
Features

- Highest brightness AllnGaP chip
- Top emitting device
- Lambertian radiation
- Grouping: radiant power

Applications

- Solid state light
- Horticulture
- Industrial applications

Delineation



Mechanical characteristics

DESCRIPTION		MINIMUM	Typical ¹	Махімим
Chip size	(µm)	1395	1410	1425
Chip height	(µm)	105	120	135
Bond pad diameter	(µm)	130	140	150
Top contact		Cathode (n), Au		
Bottom contact		Anode (p), Au		
Die attach		Epoxy bonding		

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Electro-optical characteristics (T $_{A}=25^{\circ}\text{C})^{2}$

PARAMETER	SYMBOL	Condition	MIN.	TYP. ¹	Max.	Unit
Forward voltage Reverse voltage	V _F V _R	$I_F = 700 \text{mA}$	2.0	2.1	2.4	V
Centroid wavelength Radiant power	11	$I_F = 700 \text{mA}$ $I_F = 700 \text{mA}$	645 800	655 950	665 1250	nm mW

Maximum ratings ($T_A = 25^{\circ}\text{C}$)³

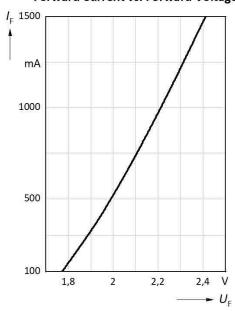
5	SYMBOL		V	ALUE		Unit
Т	- op		-40.	125		°C
	•		-40.	125		$^{\circ}C$
_	_			150		$^{\circ}C$
I	=		100	1000		mA
I _P		1500		mA		
WAVE	LENGTH (NM)				
645-	647.5-	650-	652.5-	655-	657.5-	660-
650	652.5	655	657.5	660	662.5	665
W36	WM36	X36	XM36	Y36	YM36	Z36
W37	WM37	X37	XM37	Y37	YM37	Z37
	WAVE 645- 650	WAVELENGTH (I 645- 647.5- 650 652.5 W36 WM36	T _{op} T _{stg} T _j I _F I _P WAVELENGTH (NM) 645- 647.5- 650- 650 652.5 655 W36 WM36 X36	Top -40. Tstg -40. Tj IF 100 IP WAVELENGTH (NM) 645- 647.5- 650- 652.5- 650 652.5 655 657.5 W36 WM36 X36 XM36	Top -40125 Tstg -40125 Tj 150 IF 1001000 IP 1500 WAVELENGTH (NM) 645- 647.5- 650- 652.5- 655- 650 652.5 655 657.5 660 W36 WM36 X36 XM36 Y36	Top -40125 Tstg -40125 Tj 150 IF 1001000 IP 1500 WAVELENGTH (NM) 645- 647.5- 650- 652.5- 655- 657.5- 650 652.5 655 657.5 660 662.5 W36 WM36 X36 XM36 Y36 YM36

There may be more than one bin on one single foil. Single bins cannot be ordered. Customized specifications are possible. Please contact our nearest sales office for further information.

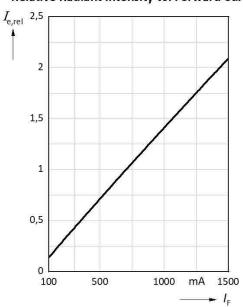


Typical characteristics graphs

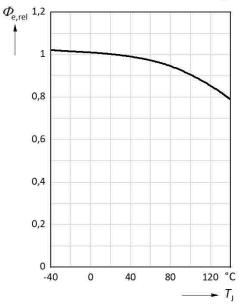
Forward Current vs. Forward Voltage



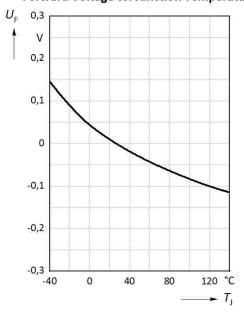
Relative Radiant Intensity vs. Forward Current



Relative Radiant Flux vs. Junction Temperature

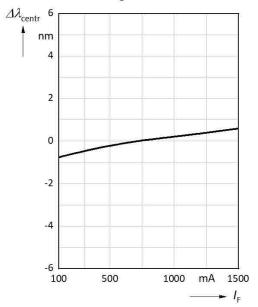


Forward Voltage vs. Junction Temperature

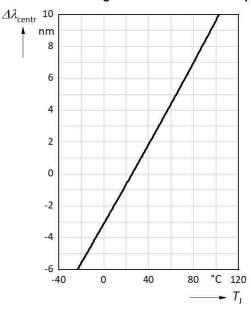




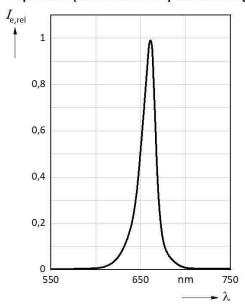
Centroid Wavelength Shift vs. Forward Current



Centroid Wavelength Shift vs. Junction Temperature



Spectrum (Relative Intensity vs. Wavelength)



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Handling and storage conditions

Storage time for wafers in sealed condition is not limited by the die itself, but may be limited by the adhesion of the blue foil (storage ambient conditions: $T_a = 15 \dots 30^{\circ}$ C; relative humidity: < 60%, vertical storage). Customer has to make sure that there is no glue from the adhesive foil on the backside either by a die shear test or by visual inspection of the backside before production. The hermetically sealed shipment lot shall be opened under temperature and moisture controlled cleanroom environment only. Customers have to follow the according rules for desposition as the material can be hazardous for humans and the environment. Chips are placed on a blue foil, which may contain the following substance in a concentration of circ.18% wt: Bis (2-ethyl(hexyl)phthalate) (DEHP) [CAS #: 117-81-7; EC # 204-211-0]. Dice have to be handled ESD sensitive.

Packing

Chips are placed on a blue foil inside a 6 inch ring or alternatively on a blue foil (mylar). For shipment the wafers of a shipment lot are arranged to stacks. Please use the recycling operators familiar to you. If required you can ask for our help. Please get in touch with your nearest sales office. By agreement we will take packing material back, if sorted. Transport costs of any kind must be paid by customers. For packing material that is returned to us unsorted or which we are not obliged to accept, any costs incurred will be invoiced to you.

Design objectives

The chip design was developed and released based on the producer's standard assembly procedures and packaging. Bond strength properties are in accordance to MIL-STD-750D, method 2037. Whether the chip fits to the customer's products with its according die and wire bond procedures and packaging must be evaluated by the customer himself. If workability problems arise after this release a mutually conducted problem solving procedure has to be set up, if the chips are suspected of contributing to the problems. The chips are produced with best effort, but on chip level a subset of the chip characteristics can be determined only. Performance of the chip in the customer's products can only be determined by the customer himself.

Returns and complaints

For complaints and returns of material a RMA-number is necessary. Samples for analysis purposes can be send to us without credit.

Shipping conditions

If not otherwise arranged, the "General Terms of Business of Chips 4 Light GmbH" apply for any shipment. If this document is not familiar to you, please request it at our nearest sales office.

LED Chip Hyper red (655 nm) 56 mil



Disclaimer Attention please!

- Components used in life-support devices or systems must be expressly authorized for such purpose!
 - Critical components⁴ may only be used in life-support devices⁵ or systems with the express written approval by us.
- All products, product specifications and data to improve reliability, function, design or otherwise are subject to change without notice .
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- The information describes the type of component and shall not be considered as assured characteristics. Due to technical requirements components may contain dangerous substances. For information on the types in question please contact our Sales Organization.
- Lead free product RoHS compliant.
- The quality level of the final visual inspection shall comply to an AQL of 1.0 (according to MIL-STD-105E, level II), if the customer performes an incoming visual inspection of a shipment.

LED Chip Hyper red (655 nm) 56 mil



- All chips are checked according to the producer's specification of the visual inspection. If this document is not familiar to you, please request it at our nearest sales office.

Changes

_	VERSION	DATE	CONTENT
	1.0	21.01.2021	Change management has started, technical graphs have been added (page 3-4).
2	2.0	21.01.2022	New address.

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¹Due to the special conditions of the manufacturing processes of LED, the typical data or calculated correlations of technical parameters can only reflect statistical figures. These do not necessarily correspond to the actual parameters of each single product, which could differ from the typical data and calculated correlations or the typical characteristic line. If requested, e.g. because of technical improvements, these typ. data will be changed without any further notice.

 $^{^2}$ Measurements are done with an accuracy of $\pm 15\%$. Correlation to customer's equipment and products is required.

³Maximum ratings are package dependent and may differ between packages. The forward current is not limited by the die but by the effect of the LED junction temperature on the package. If you need more information on pulsed operation, please contact your next sales office about possible driving conditions. If not otherwise specified the maximum pulse current may not exceed the maximum current in continuous mode.

⁴A critical component is a component used in a life-support device or system whose failure can reasonably be expected to cause the failure of that life-support device or system, or to affect its safety or the effectiveness of that device or system.

⁵Life support devices or systems are intended(a) to be implanted in the human body,or(b) to support and/or maintain and sustain human life. If they fail, it is reasonable to assume that the health and the life of the user may be endangered.